1. Suppose we have two implementation of same instruction set architecture. Computer A has a clock cycle time of 250 PS and a CPI of 1.8 for some program, and computer B has a clock cycle time of 500 PS and a CPI of 1.2 for the same program. Which computer is faster and how much? 7
2. Draw the entire instruction format for the MIPS. 3
3. For the following high level statement write the MIPS machine Code. 10

A [x] = i + A [32] - j; where x= last two digit of your id number.